



2020 Functional Safety Working Group (FSWG)

Virtual Event#2 Day#2b (Day 2)
November 19, 2020 - 2:00PM - 6:00PM CET

Preliminary Agenda

Topic	Time
Versal Functional Safety Architecture	2:00 - 3:00PM
ASIL D in Versal & Zynq UltraScale+	3:00 - 3:30PM
Break	3:30 - 3:45PM
What to Know Before Starting a Design?	3:45 - 4:30PM
Security in Functional Safety Applications	4:30 - 5:15PM
Xilinx Functional Safety Roadmap	5:15 - 6:00PM

Event Wrapup